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ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.

PATENT DATE

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APPLICATION NO. 09/918511	CONT./PRIOR F	CLASS 438	SUBCLASS 32.00	ART UNIT 2812	EXAMINER Issac
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APPLICANTS

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title

Manufacturing method of thin film transistor panel

[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS <div style="display: flex; justify-content: space-between;"> <div style="width: 30%;">Sheets Drawg.</div> <div style="width: 30%;">Figs. Drawg.</div> <div style="width: 30%;">Print Fig.</div> </div>			CLAIMS ALLOWED <div style="display: flex; justify-content: space-between;"> <div style="width: 45%;">Total Claims</div> <div style="width: 55%;">Print Claim for O.G.</div> </div>		
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	<div style="border-bottom: 1px solid black; height: 20px; margin-bottom: 5px;"></div> <div style="display: flex; justify-content: space-between; font-size: small;"> (Assistant Examiner) (Date) </div>			NOTICE OF ALLOWANCE MAILED		
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